

Figure 1A

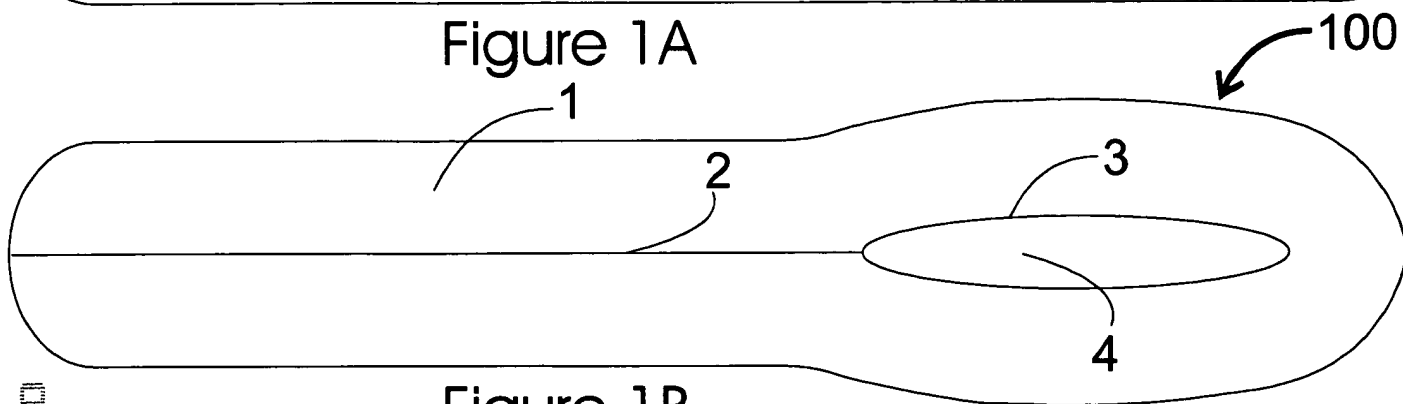


Figure 1B

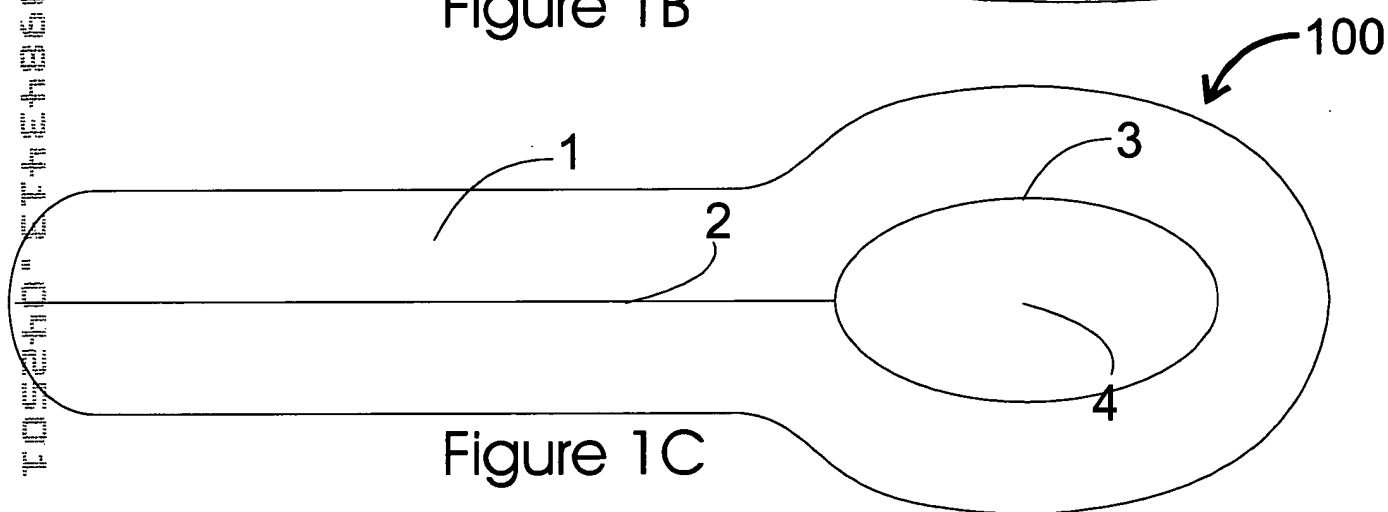


Figure 1C

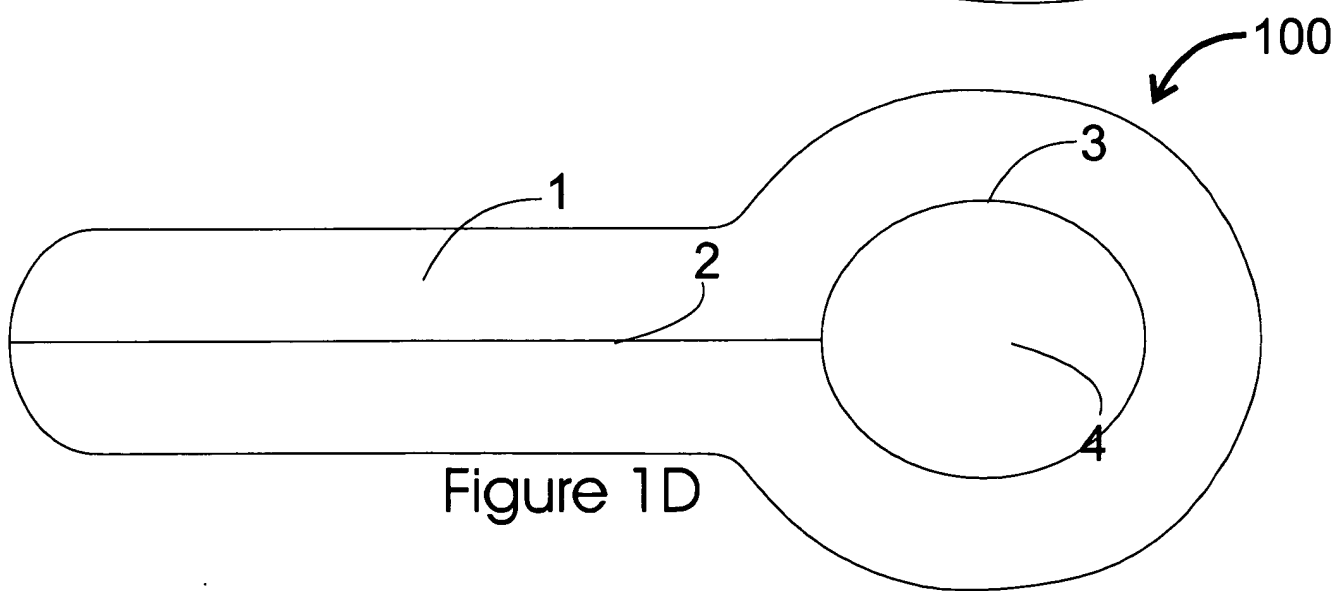


Figure 1D

FIG. 1A-1D

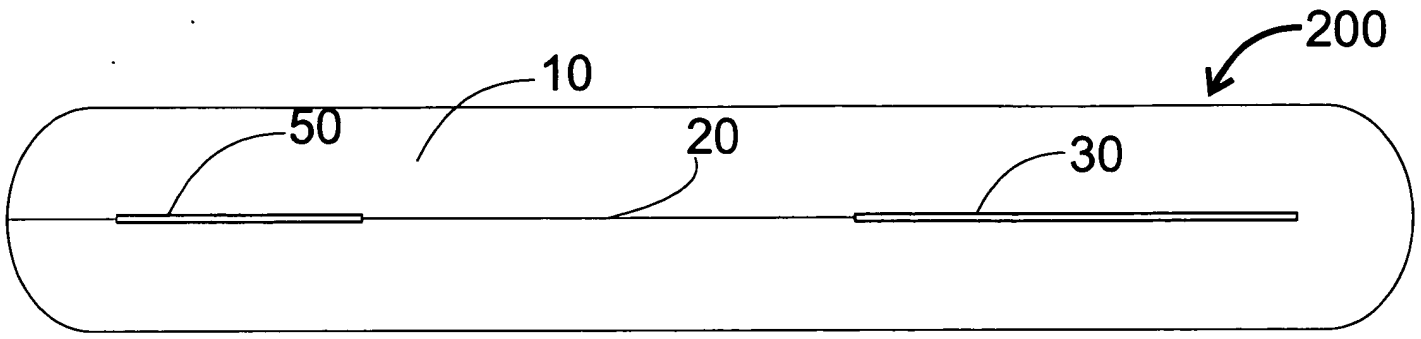


Figure 2A

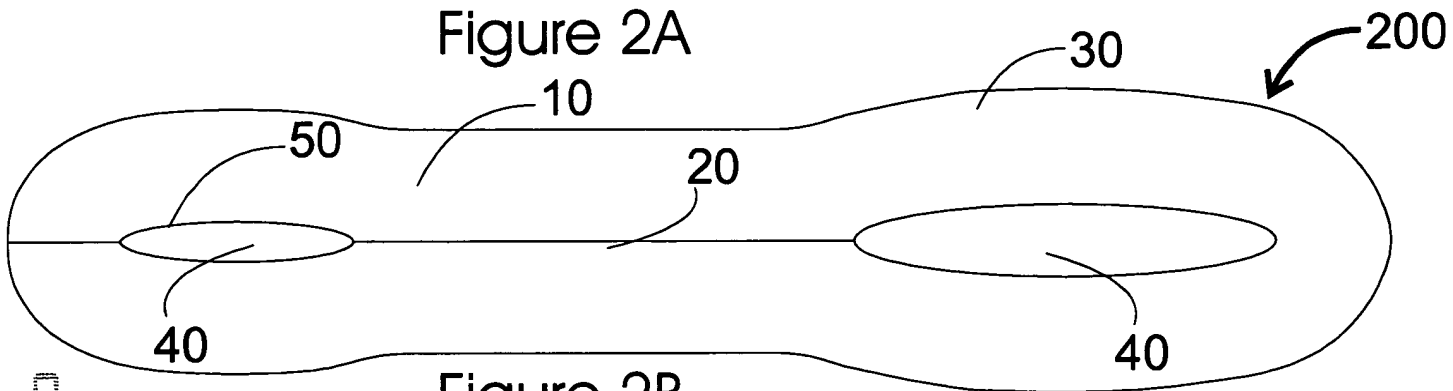


Figure 2B

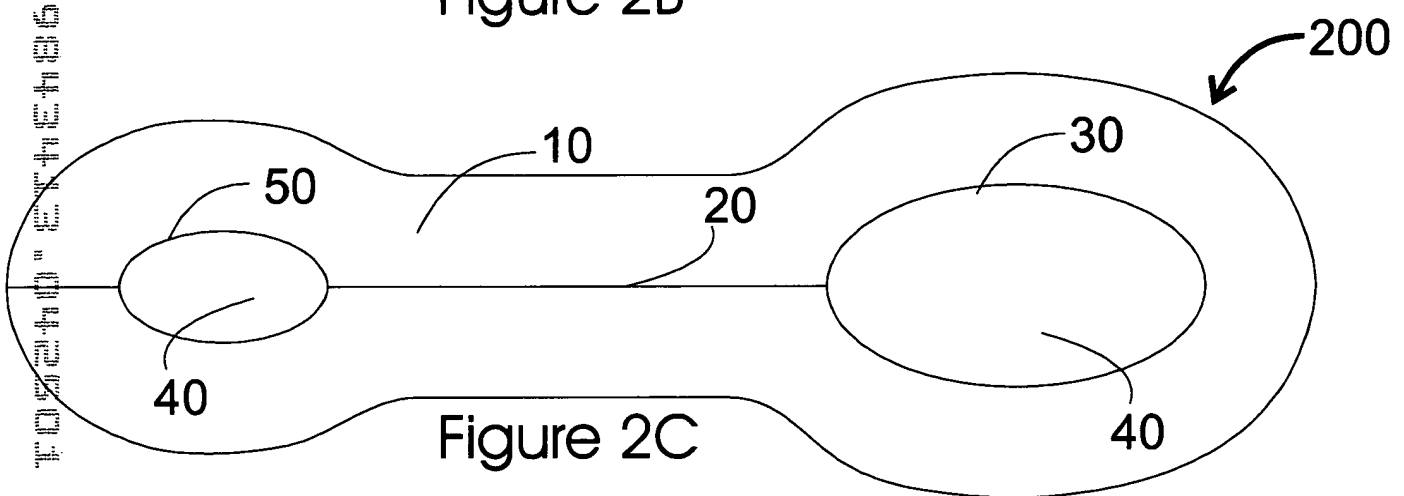


Figure 2C

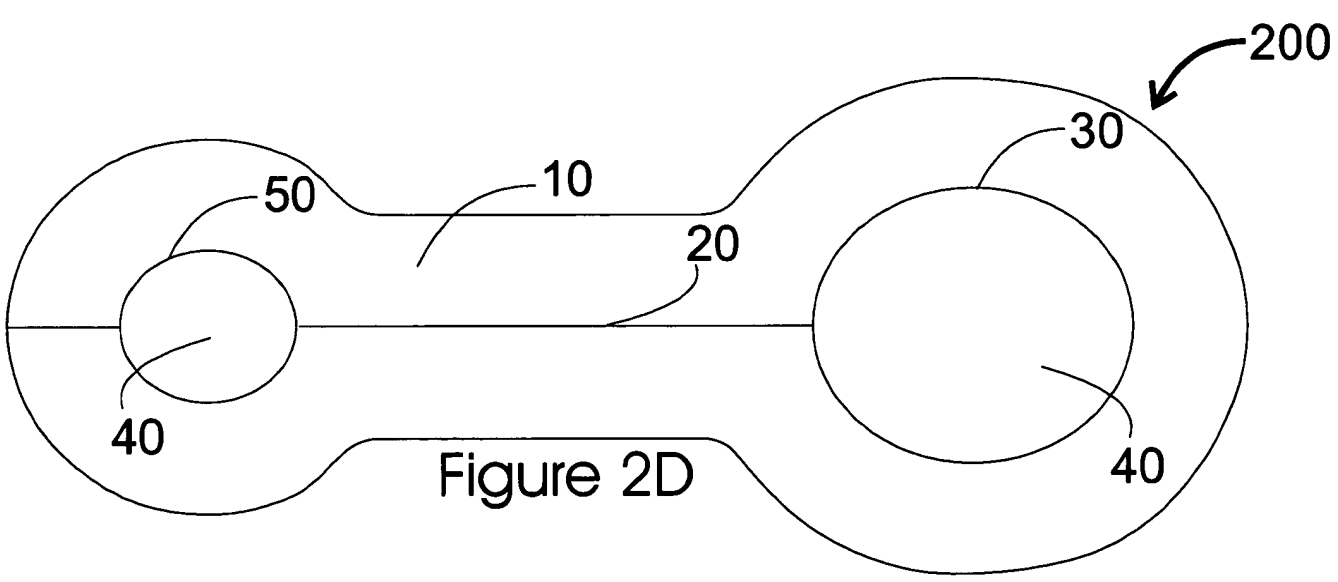
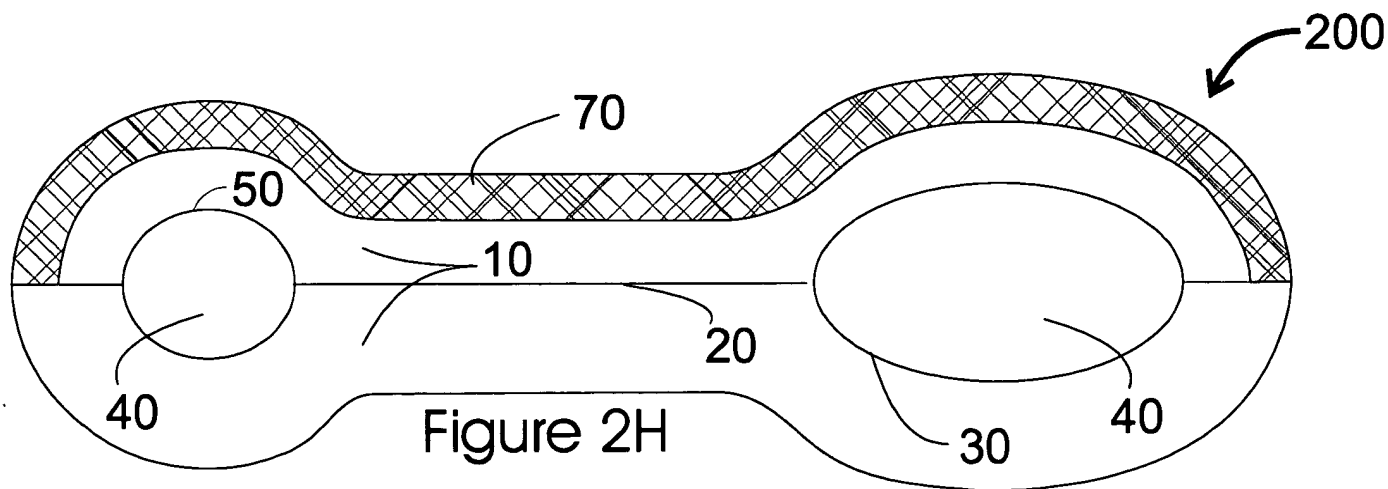
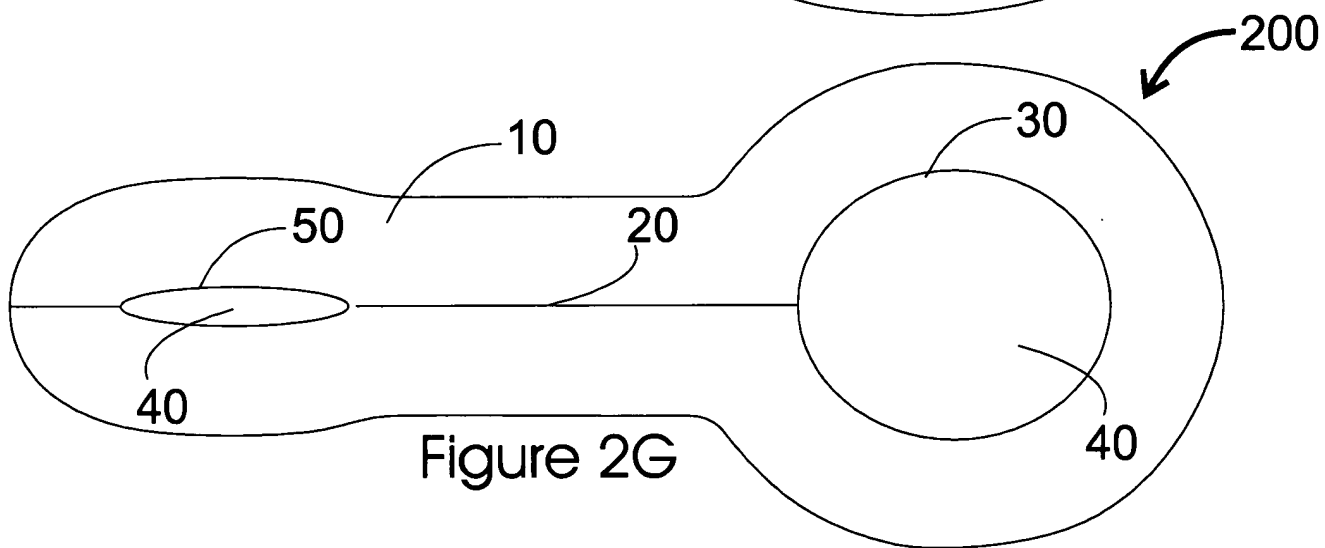
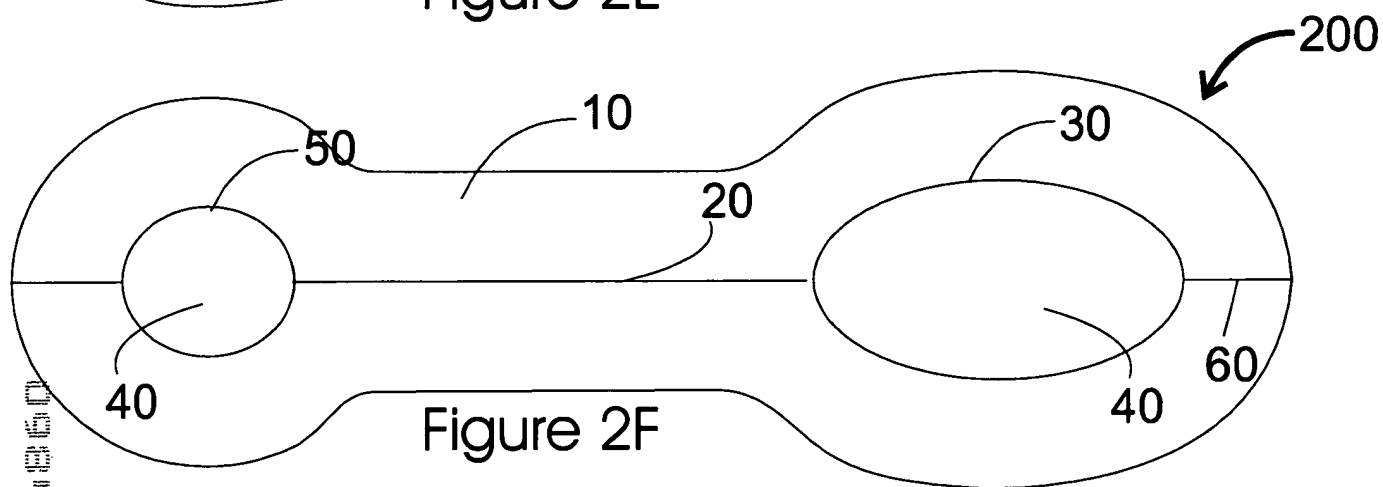
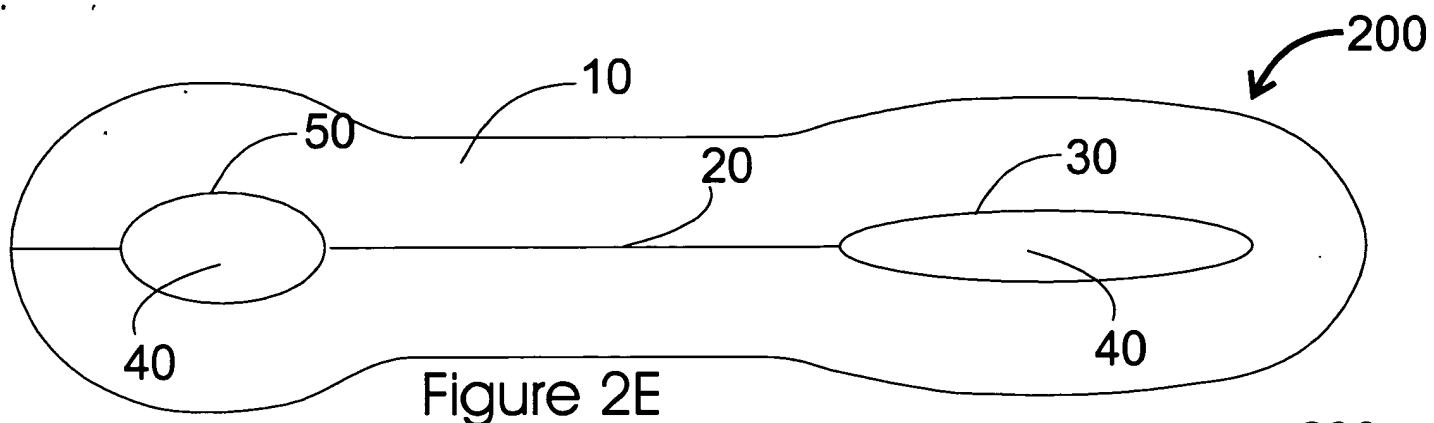


Figure 2D

FIG. 2A-2D are schematic diagrams of a device 200 in various configurations.



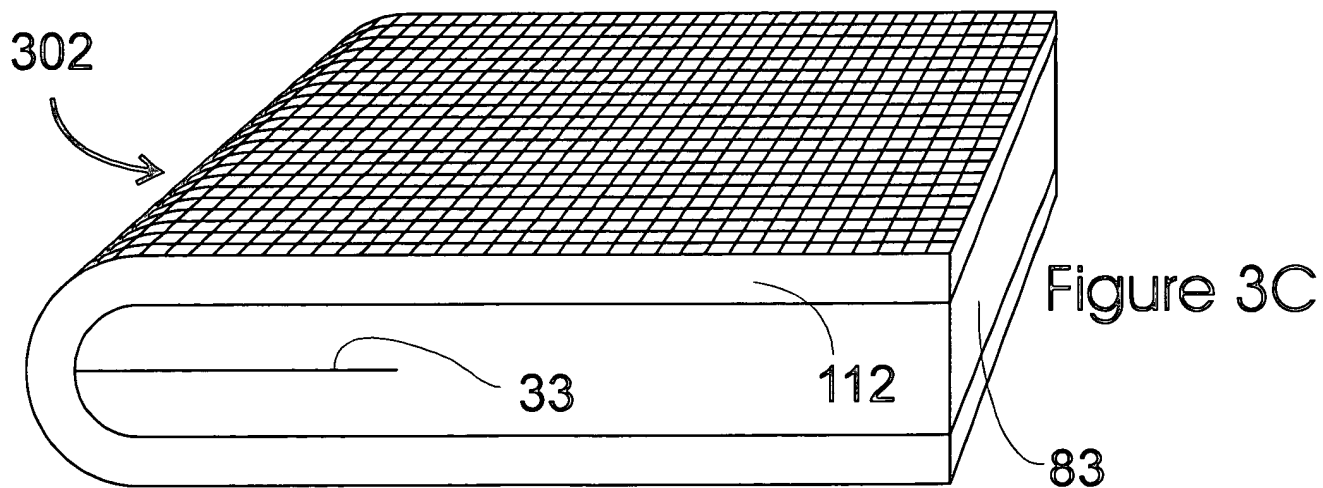
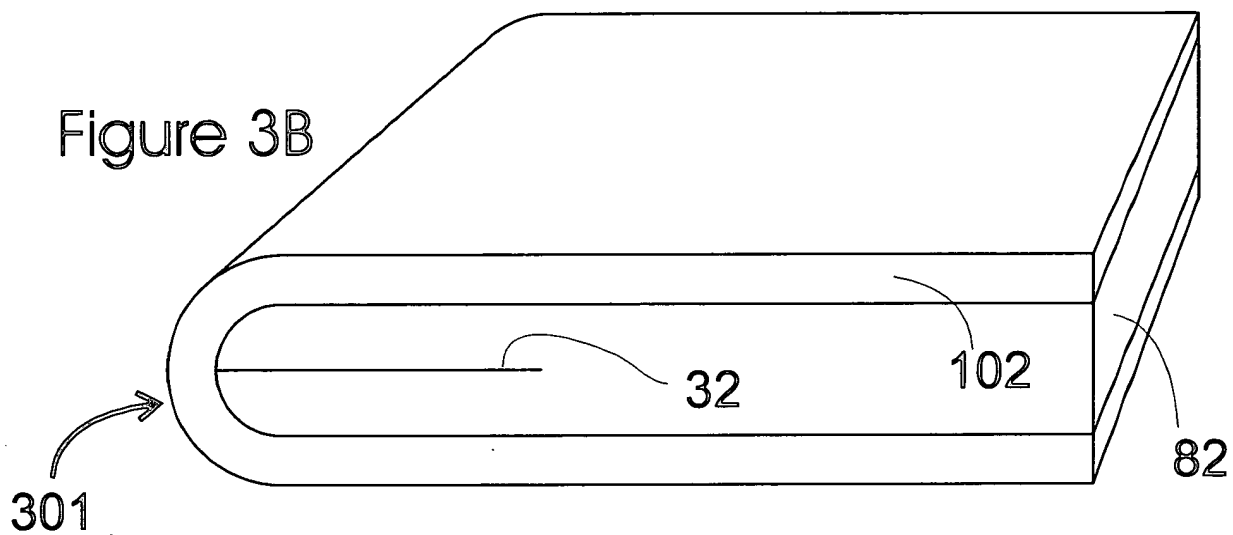
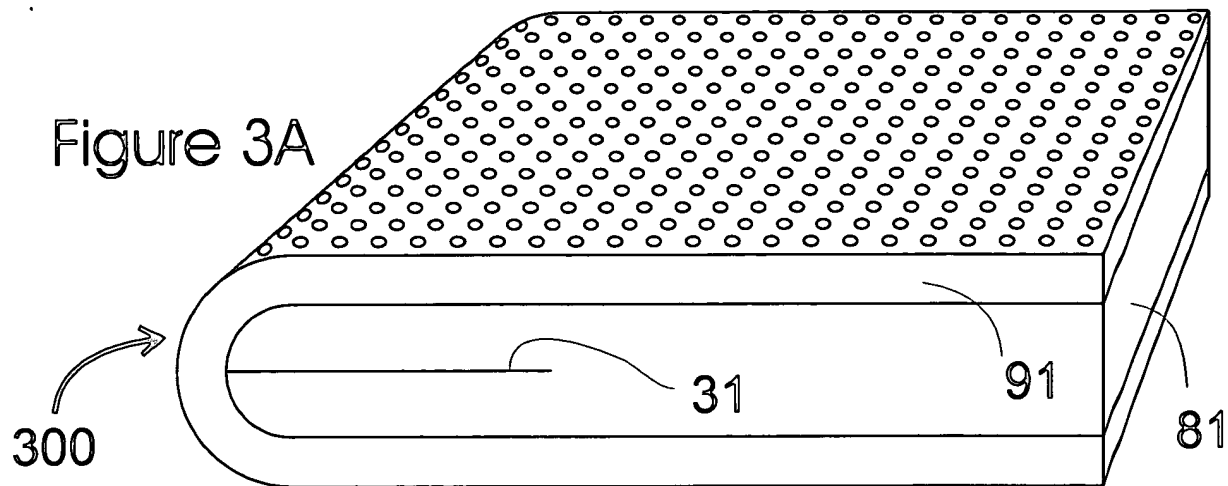


Figure 4A

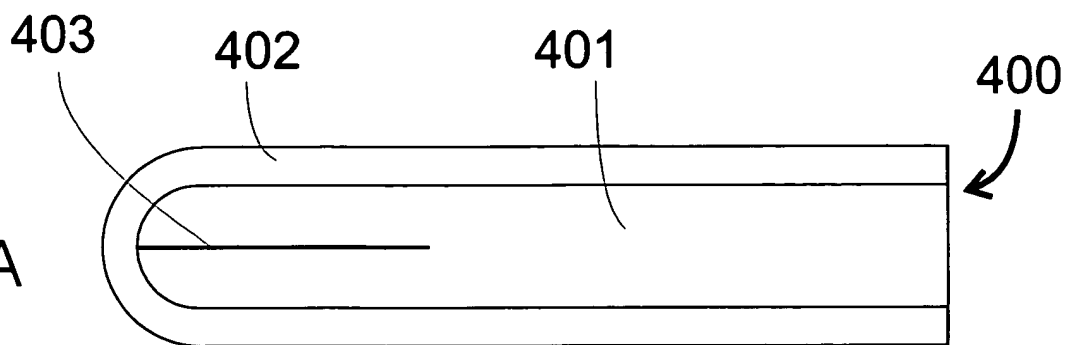


Figure 4B

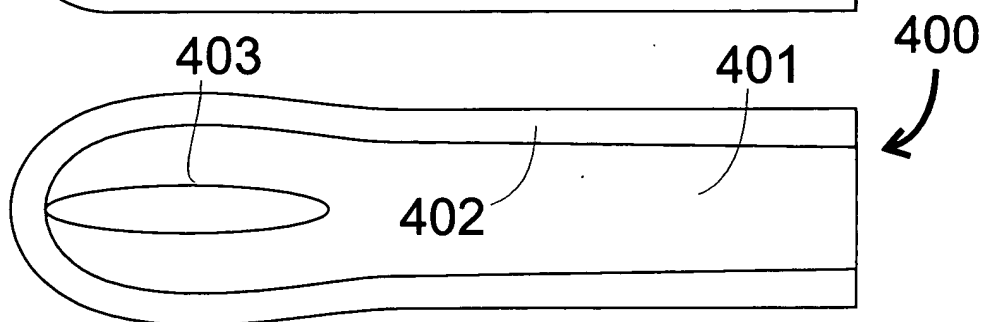


Figure 4C

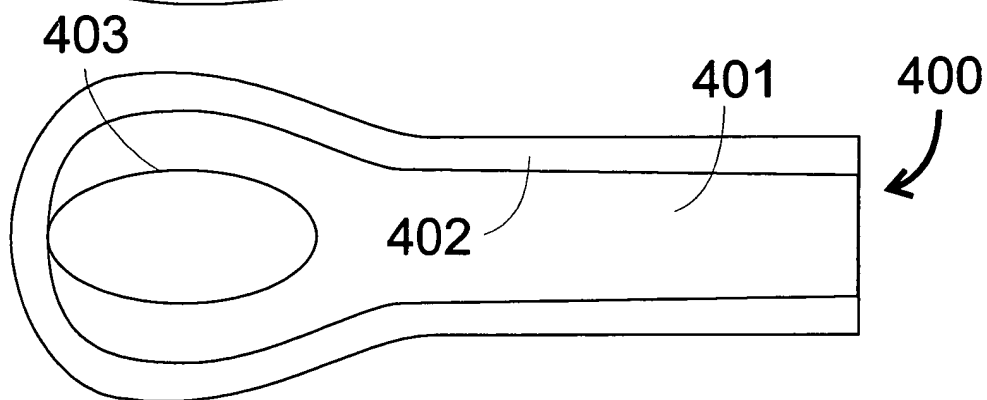


Figure 4D

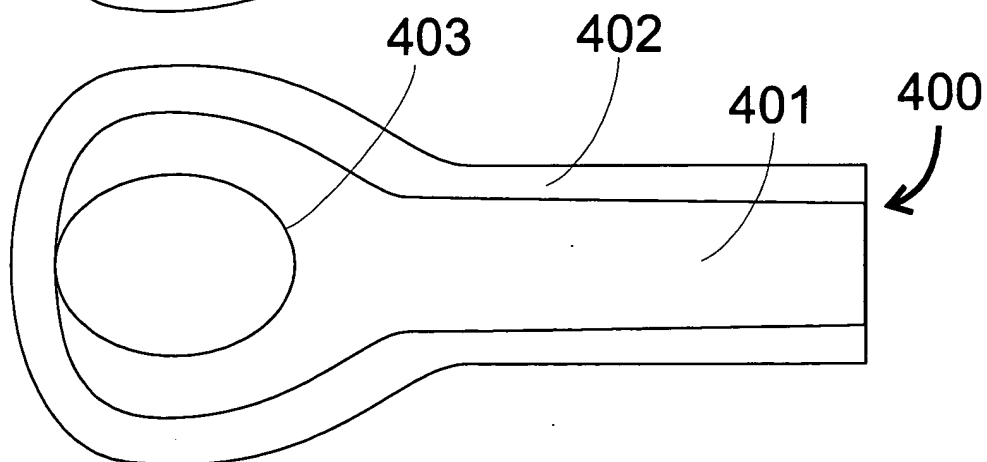


Figure 5

